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WELCOME TO ARC 2016, THE 12TH INTERNATIONAL SYMPOSIUM ON APPLIED RECONFIGURABLE COMPUTING

Mangaratiba, Rio de Janeiro, Brazil, 22-24 March, 2016

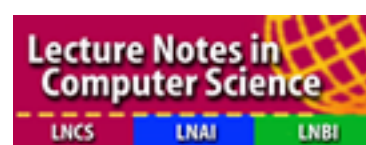
Hosted by the University of São Paulo, Institute of Mathematical and Computer Sciences

Paper Submission Deadline: Extended to Monday, November 16, 2015

APPLIED RECONFIGURABLE COMPUTING (ARC) SYMPOSIUM

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via customizing, even at runtime, the topology of the underlying architecture to match the specific needs of a given application. Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures.

The International Symposium on Applied Reconfigurable Computing (ARC) aims to bring together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. This year's Symposium will have a series of international invited speakers who will express their views on the future of reconfigurable technology. The accepted papers will be included in the proceeding published by Springer Lecture Notes in Computer Science (LNCS), which will be indexed by ISI Proceedings and EI-Compendex. Additionally, selected papers will be invited to be submitted for consideration for a special section of the Elsevier Journal - Microprocessors and Microsystems: Embedded Hardware Design (MICPRO).



THEMES

Authors are invited to submit original contributions in English on a wide variety of topics related to applied reconfigurable computing, including but not limited to those listed below.

Applied RC Design Methods & Tools

- High-Level Compilation
- Simulation
- Estimation Techniques
- Design Space Exploration
- Programming Languages and paradigms for reconfigurable computing
- Run-Time Systems
- Compilation and Synthesis

Applied RC Applications

- High-Performance Systems
- Embedded Computing
- Robotics
- Digital Signal Processing
- Safety and Mission Critical Systems
- Biomedical
- Other

Applied RC Architectures

- Fine-grained and Mixed-grained
- Multi-processor-based
- Embedded Multi-Core
- Reconfigurable Fabric Architectures
- Resilient through Reconfiguration
- Fabric Architectures
- System Architectures and Integration
- Other

Critical Issues in Applied RC

- Teaching Reconfigurable Computing
- Surveys and Future Trends
- Benchmarks