# **Call for Papers**

The 14th International Symposium on Applied Reconfigurable Computing (ARC 2018) 2 - 4 May 2018, Santorini, Greece http://arc2018.esda-lab.cied.teiwest.gr



### Symposium Information

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via customizing, even at runtime, the topology of the underlying architecture to match the specific needs of a given application. Contemporary adaptive systems allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient faults in emerging device structures.

ARC aims at bringing together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. ARC 2018 will have a series of international invited speakers that will express their view on the future of reconfigurable technology.



The ARC 2018 proceedings will be published by Springer-Verlag as a Lecture Notes in Computer Science (LNCS) series volume and will also be available through the Springer Link on-line service

Selected papers will be invited to be submitted for consideration in the special issue of a high quality journal that will be announced via the web site of the conference during the next weeks

### **Organizing Committee**

#### **General Chairs:**

• Nikolaos Voros (Technological Educational Institute of Western Greece)

• Michael Hübner (Ruhr Universität in Bochum, Germany)

#### Program Chairs:

- Georgios Keramidas (Technological Educational Institute of Western Greece)
- Diana Goehringer (TU Dresden, Germany)

Proceedings Chair:

• Pedro Diniz (INESC-ID, Lisboa, Portugal)

### Web Chair:

 Christos Antonopoulos (Technological Educational Institute of Western Greece)

#### **Publicity Chairs:**

- Luigi Carro (UFRGS, Brazil)
- Stephan Wong (TUDelft, The Netherlands)
- Chao Wang (USTC, China)
- Dimitrios Soudris (NTUA, Greece)

### Submission Information

are invited to submit original Authors contributions in English including, but not limited to, the areas of interest mentioned below. Submission must be uploaded to the ARC website and identify the format of the contribution as either:

- Long Papers: (12 pages maximum) should include mainly accomplished results (oral presentation)
- *Short Papers:* (6 pages maximum, or 8 pages max. upon payment of additional charge of 50 EUR per extra page) to be composed of work in progress or reporting recent developments (poster presentation)

The format of the paper should be according to the Springer-Verlag LNCS Series format rules

**European Projects Section:** The ARC 2018 conference will include an extra section of the Proceedings dedicated to European Projects.

### Important Dates

#### Submission deadline 11 November 2017

Author Notification 19 December 2017

Camera-Ready and Author Registration 15 January 2018

### EU Projects Track Chair :

Christos Antonopoulos (Technological Educational Institute of Western Greece )

Trends (in)

Teaching RC

**Benchmarks** 

Trends

Surveys and Future

Emerging Technologies

Cyberphysical systems

TECHNISCHE

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## **Topics of Interest**

#### RC Design Methods & Tools **RC** Architectures

https://goo.gl/oS65DV

- High-level Languages & Compilation
- Simulation & Synthesis
- Estimation Techniques
- Design Space Exploration Run-Time Systems &

**Organized by:** 

in

- Virtualization
- Approximate Computing Fine-/Coarse-/Mixed-grained

Self-adaptive, evolvable

- Embedded MPSoCs and NoCs
- **Resilient & Fault Tolerant**
- Safety & Mission Critical Systems Deep Learning & Neural Networks

Automotive

**RC** Applications

Big Data & HPC

Security & Cryptography

Embedded Computing & DSP

Robotics, Space, Bioinformatics,







https://qoo.ql/5Lh5eZ

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