ARC 2021
29 June – 1 July
Rennes, France

International Symposium on Applied Reconfigurable Computing

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Due to pandemic crisis, ARC 2021 will be a virtual event and free of charge!

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via customizing the topology of the underlying architecture to match the specific needs of a given application, even at run time. Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit width, and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures. The international symposium on Applied Reconfigurable Computing (ARC) aims to bring together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology.

The ARC 2021 proceedings will be published as a volume in Springer's Lecture Notes in Computer Science (LNCS) and will also be available through the SpringerLink online service.

The authors of selected papers will be invited to submit extended versions of their work to a special issue in Springer Journal of Signal Processing Systems.

Important dates

Extended paper submission:
~~22 Feb. 2021~~ **15 Mar. 2021**
Tutorial proposals: 31 Mar. 2021
Author notification: 23 Apr. 2021
Camera-ready: 10 May 2021

Submission

CFP

Easychair

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